NORTH MAHARASHTRA UNIVERSITY, JALGAON (M.S.)

MASTER OF ENGINEERING (M.E.)

ELECTRONICS AND COMMUNICATION,
ELECTRONICS AND TELECOMMUNICATION
(Communication Engg.)

W.E.F 2010 - 2011
North Maharashtra University Jalgaon
ME Electronics and Telecommunication / Electronics and Communication (Communication Engg)
(W.E.F: 2010-11)

Structure

First Year Term-I

<table>
<thead>
<tr>
<th>Sr No</th>
<th>Subject</th>
<th>Teaching Scheme Hours/Week</th>
<th>Examination Scheme</th>
<th>Maximum Marks</th>
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<tr>
<td></td>
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<td>Lectures</td>
<td>Practical</td>
<td>Paper Duration Hours</td>
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<tr>
<td>1</td>
<td>Advanced Digital Signal Processing &amp; Processors</td>
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<tr>
<td>2</td>
<td>Advanced VLSI Design</td>
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<td>3</td>
<td>Advanced Light Wave Communication</td>
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<td>Advanced Communication Systems</td>
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<td>Elective-I</td>
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<tr>
<td>6</td>
<td>Laboratory Practice-I</td>
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<td>-</td>
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<td>7</td>
<td>Seminar-I</td>
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First Year Term-II

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<td>Communication System Design</td>
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<td>Mobile Communication</td>
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<td>Antenna Theory &amp; Design</td>
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### Elective for First Year Term I & II

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<td>Modeling and Simulation</td>
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<td>Multimedia Communication</td>
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<td>Advanced Telecomm Network Management</td>
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<td>Error Control Codes</td>
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<td>Linear System Theory</td>
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<td>Optimization Technique</td>
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<td>Speech and Audio Processing</td>
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### Second Year Term - I

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### Second Year Term - II

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<td>Lectures</td>
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<td>Progress Seminar</td>
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INTRODUCTION AND TRANSFORMS: Discrete time signal and systems, its representation, types of discrete time system, DFT, IDFT, FFT (DIF and DIT), Wavelet Transform.

Power spectral estimation - parametric and nonparametric method for power spectral estimation, minimum variance.

Realization of FIR and IIR filter.

Design of digital filters-symmetric and anti-symmetric, linear phase, optimum, Equi-ripple, FIR differentiation, Hilbert’s transformers.

Design of FIR filters using different Window technique.

Design of IIR filters-impulse invariance, bilinear transformation, matched transformation, frequency transformation in analog and digital domain.

Least mean square Adaptive filter: Overview of the structure, operation of the LMS algorithm, LMS adaptive algorithm, statistical LMS theory, Comparison of the LMS algorithm with the steepest Descent algorithm, Computer experiment on adaptive prediction, Computer experiment on adaptive equalization, Computer experiment on a minimum- variance distortion less response beam former, Directionality of convergence of the LMS algorithm for Nonwhite Inputs, Robustness of the LMS filter, Upper bound on the step size Parameters for Different Scenarios, Transfer function approach for deterministic input summary problems.

Multirate digital signal processing-decimation by factor D, interpolation, filter design and implementation, sampling rate conversion, application of multirate signal processing.

Design of digital filters based on least square method.

Application of DSP to speech processing and radar signal processing.

Introduction to TMS320c62XX DSP processors.

REFERENCES:

3. L.R. Rabiner & B. Gold, Theory & application of digital signal processing- Prentice Hall
4. A. Antinoiu, Digital Filters; analysis, design & application- Mcgraw Hill
5. Salivahanan, vallavaraj gnanapriya, Digital Signal Processing-TMH
**North Maharashtra University Jalgaon**
M.E. Electronics and Telecommunication / Electronics & Communication (Communication Engg)
W.E.F: 2010-11
First Year Term – I

**ADVANCED VLSI DESIGN**

**Teaching Scheme:**
Lectures: 3 hrs / week

**Examination Scheme:**
Theory Paper: 100 Marks (3 Hours)

**Review of HDL Programming:** Hardware modeling with VHDL, different VHDL constructs, and Logic Synthesis. Levels of abstraction, different modeling styles: Data flow, Behavioral, Structural, Mixed and switch level Description.

**Designing steps in ASIC:** Physical Design flow, Different type of ASIC, CAD Tools, System Partitioning, Estimating ASIC size, Power dissipation, FPGA Partitioning methods, Floor planning, Placement Physical design flow; Information Formats; global routing, detailed routing; special routing; circuit extraction and DRC

**Introduction to CMOS circuits:** MOS Transistor, CMOS Logic The inverter, combination logic, The NAND and NOR Gates, Compound gates, Multiplexers, Memory Latches and Registers, Circuit and Systems representation and examples Behavioral, Structural and Physical representation.

**Review of MOS Transistor Theory:** n-MOS and p-MOS Enhancement transistor, Threshold voltage equations, Body effects, MOS device Design equations, Basic DC equations, second order effects, MOS Models, Complementary CMOS inverter DC Characteristics, Noise margin, Static load MOS Inverters, Differential Inverter, the transmission gate, Tristate Inverter, Bi-CMOS Inverters.

**CMOS Processing Technology:** Basic CMOS technology, n and p well processes, CMOS Process Enhancements, Layout design rules, Technology related CAD issues

**Circuit Characterization and performance Estimation:** Resistance and capacitance estimation, Inductance m switching characteristics analytical, empirical delay models, gate delays, CMOS Gate transistor sizing, Power dissipation, Sizing routing conductors, Charge sharing, Yield, reliability, Scaling of MOS Transistor dimensions

**CMOS Circuit and Logic Design:** CMOS logic gate design, Basic Physical design of simple logic gates, CMOS logic structures, Clocking strategies, I/O structures, Low power design.

**References:**

10. Ivan Sutherland “Logical effort”, Morgan Kauf
ADVANCED LIGHT WAVE COMMUNICATION

Teaching Scheme: Lectures: 3 hrs / week
Examination Scheme: Theory Paper: 100 Marks (3 Hours)

**Light Wave Components:** Optical sources, ternary materials, qua-ternary materials, detectors, APD. Fiber bragg gratings, linear and non linear FBG. Electro-optic devices, magneto-optic devices, acousto-optic devices, nonlinear optical devices, filters, amplifiers.

**Light Wave Sensors:** FBG sensors and their applications, fiber sensors for current and voltage measurement.

**Light Wave Communication:** Analog and digital optical transmitter and receiver concept, loss limited and dispersion limited light wave system, telecommunication fiber link, dispersion compensation techniques, power and rise time budget, power penalty. Radio over fiber technology, microwave photonics, wireless optical communication system.

**Coherent Light Wave System:** Modulation and demodulation scheme for coherent communication, heterodyne synchronous and non synchronous detection, homodyne detection, phase diversity reception, receiver sensitivity, single and multicarrier system.

**Mutichannel Light Wave System:** WDM components and devices, multiplexing techniques and system performance issues, WDM network design, cost tradeoffs, LTD and RWA problems, routing and wavelength assignment, wavelength conversion.

**Soliton Communication:** Fiber solitons, soliton based communication principles, soliton amplification, soliton system design, WDM soliton systems.

**Light Wave Networks:** Network topologies, SONET / SDH, single hop networks, multihop networks, wavelength routed network, optical CDMA, photonic packet switching, ultrahigh capacity networks, network management functions, management frame work, ATM function, adaptation layers, frame structure, quality of services and flow control, ESCON, HIPPI.

**References:**

ADVANCED COMMUNICATION SYSTEMS

Teaching Scheme: Examination Scheme:
Lectures: 3 Hrs / week Theory Paper: 100 Marks (3 Hours)

Characterization of Communication Signal and Optimum Receiver for AWGN Channel:
Signal space representation, Memory less modulation methods, Linear Modulation with memory, Non linear modulation methods with memory, CPFSK and CPM, Power spectral of linear modulated signal, CPFSK and CPM Signals, Correlation demodulator, Match filter demodulator, Optimum detector, Probability of error for binary and M-array signals.

Source and Channel Coding: Discrete stationary sources, LZ algorithm, Coding of analog sources, Rate distortion functions, Scalar quantization and vector quantization, Temporal and spectral waveform coding, BCH codes, Reed Solomon codes, Reed Muller codes, convolution codes, transfer function of convolution codes, Viterbi decoding algorithm, stack algorithm (no problems expected) trellis coded modulation.

Signal Design for Band Limited Channel and Equalization. Design of band limited signal for zero ISI, Nyquist criterion, design of band limited signal for controlled ISI, partial response signal. Data detection for controlled ISI, Linear Equalization – peak distortion criteria, mean square error (MSE) criteria, decision feedback equalization, coefficient optimization, adaptive linear equalization, zero forcing algorithm

Spread Spectrum Techniques: Generation of PN sequence, direct sequence spread spectrum system, processing gain, jamming margin, application of direct sequence spread spectrum signal, frequency hopped spread spectrum signal, time hopping spread spectrum signal, synchronization of spread spectrum signal - acquisition and tracking.

Text Books:

References:
5. Stephen Wicker, “Theory of Error Correcting Codes”, PHI
Teaching Scheme:
Lectures: 3 hrs / week

Examination Scheme:
Theory Paper: 100 Marks (3 Hours)

Methods of solution of network, Network equations and formulations, DC, AC and transient analysis of networks, Simulation examples using Spice or other relevant packages.

Types of modeling, Models of diode, BJT and FET, Design and simulation of Logic circuits and analog circuits, Sensitivity and optimization of networks and functions.

Features, levels of abstraction, elements, simulation process, types of simulators, FSM modeling, test benches, generics and attributes, synthesis tools features and optimization in VHDL, Synthesis guidelines, Timing issues: terminology, flow diagram, clock, gated clock, setup & hold time, violation, meta stability, static & dynamic timing analysis.

CMOS and Bi-CMOS logic families and PLD architecture, Power dissipation, noise and ESD issues, clock distribution, signal connections, synchronous and asynchronous design features, and memory system design. Classification of CPLD architecture, CPLD 9500 series, Xilinx FPGA – XC4000 series, designing steps in ASIC.

References:
4. Douglas Perry, “VHDL”, MGH
North Maharashtra University Jalgaon  
M.E. Electronics and Telecommunication / Electronics & Communication (Communication Engg)  
W.E.F: 2010-11  
First Year Term – I (Elective I)  
ADVANCED TELECOM NETWORK MANAGEMENT

Teaching Scheme:  
Examination Scheme:  
Lectures: 3 Hrs / week  
Theory Paper: 100 Marks (3 Hours)

Management Platforms:  
The Well designed Platform system, Methods and tools, Standards and platform building blocks. Management of Personal communication systems Managing Mobile Networks from cellular to satellite networks, Digital and analogue microwave radio systems: System protection, FM analogue microwave radio, Digital microwave radio - transceiver, low capacity and high capacity.

Telecom Networks:  

Traffic Engineering:  

Symmetric Ciphers:  
Overview of network security and cryptography, model for network security, classical encryption techniques, block ciphers and data encryption standard (DES), Advanced encryption standard (AES), contemporary symmetric ciphers, and confidentiality using symmetric encryption.

Public key Cryptography:  
RSA, key management, Different Hellman key exchange, elliptic curve Arithmetic, elliptic curve cryptography.

Message Authentication and Hash functions:  
Authentication requirements, authentication functions, message authentication codes, Hash functions, security of Hash functions and MACs.

Hash Algorithms:  
MD5 message Digest Algorithm, Secure Hash Algorithm, Digital Signatures and authentication protocols, Digital signature standard.

References:  
1. Viswanathan, “Telecommunication Switching Systems and Networks”, PHI.  
4. Wayne Tomasi, “Introduction to Telecommunication Voice Data Internet”, PHI.  
LINEAR SYSTEM THEORY

Mathematical Review and Introduction
Upper and lower triangular, symmetric matrices, various operations on matrices, Eigen values and Eigen vectors, similarity transformation, modal matrix, companion form, diagonal form, Cayley-Hamilton theorem, matrix functions, vectors, linear spaces, basis, orthonormal basis, norms and their properties, singular value decomposition (SVD). Limitations of conventional control theory, Introduction of modern control theory and its applications.

State Space Representation of Linear Continuous Time Systems
State variable representation of continuous time linear system, Conversion of state variable models to transfer function, Conversion of transfer function to canonical state variable models, State transition matrix and its properties, Controllability and observability, State and output feedback, Pole region assignment

Signal Processing in Digital Control

State Space Representation of Linear Discrete Time Systems
State space representation of discrete system - Decomposition of Transfer functions - Solution of discrete time system - state transition matrix - Discretisation of continuous time state equations, Characteristic equation, Eigen values and Eigen vectors, Invariance of Eigen values, Diagonalization, Jordan Canonical form, controllability and observability, Kalman's and Gilbert's tests, Controllable and observable phase variable forms, State feedback, Pole placement design

References:
1. Katsuhiko Ogata, "Modern Control Engineering", Prentice Hall of India Private Ltd.
2. Nagrath I J and Gopal M, "Control Systems Engineering", New Age International Publisher
Introduction: Historical development, Application to Engineering problems, Statement of optimization.

Classification of Optimization: Multivariable optimization with and without constraints, linear programming standard form of linear programming, Geometry, Simplex programming, revised simplex algorithm, revised simplex method.


Dynamic Programming: Multistage decision process, principle of optimality, Computational procedures in dynamic programming, Linear programming as a case of dynamic programming. Optimization application for assignment and Network problems, Pareto optimality, and Finite element based optimization.

References:
2. G. Hadly, “Linear Programming”, Welsly
3. Peithpler Philips Wilde, “An Introduction to Optimization”, PHI

Random Variables: Cumulative distribution, Joint probability density function, Statistical properties, Jointly distributed Gaussian random variables, Conditional probability density, properties of sum of random variables, Central limit theorem, Estimate of population means, expected value and variance and covariance, Computer generation of random variables.

Multiple Random Variables: joint cumulative distribution function, Joint probability density function statistical properties, Jointly distributed Gaussian random variables, Conditional probability density, properties of sum of random variables, Central limit theorem, Estimate of population means, Expected value and variance and covariance, Computer generation of random variables.


Queuing Theory: Introduction, Cost equation, steady state probabilities, Models of single server exponential queuing system with no limit and with finite buffer capacity (M/M/I, M/M/N). Queuing system with bulk service, Network of queues with open system and closed system. The M/G/I system and application of work to M/G/I.


References:
4. Athanasios Papoulis, “Probability random variables & Stochastic process” MGH
LABORATORY PRACTICE-I

Teaching Scheme: Practical: 6 Hrs per week
Examination Scheme: Term Work: 100 Marks
Oral: 50 marks

Experiments / Assignments based on any three subject out of which one should be elective from First Year Term – I syllabus. The concerned subject in-charge should frame minimum of six laboratory experiments or assignments, two from each subject.

SEMINAR-I

Teaching Scheme: Practical: 4Hrs / week
Examination Scheme: Term Work: 100 Marks

Seminar on related state of the art topic of student’s own choice approved by the department.

Term Work
The term-work and presentation of the Seminar-I will be evaluated by departmental committee consisting of guide and two faculty members of the department appointed by Director / Principal of the college as per the recommendation of the Head of the Department.
ADVANCED DIGITAL IMAGE PROCESSING

Introduction: Digital Image representation, Elements of visual perception, Image model, sampling uniform and non-uniform Sampling, Quantization-uniform and non-uniform, relationship between pixels - neighbors, connectivity, distance, Imaging Geometry-Basic Transformation, Perspective Transformations, camera model, camera calibration, stereo imaging, arithmetic and logic operations on images.


Image Compression: Compression Fundamentals, lossy and lossless compression, Fidelity criteria, Coding Redundancy, Encoding Process, Huffman Code, RLE, 1-D/2-D Run Length Encoding, Coding Considerations. Image standards: JPEG, JPEG2000, MPEG Standards, Various Image file formats such as - BMP, TIFF, GIF.


Patterns and Pattern Classes: Recognition based on decision theoretic methods, matching, optimum statistical classifiers, neural networks, structural methods: matching shape numbers, string matching, syntactic recognition of strings, syntactic recognition of trees.

References:
4. K P Soman K I Ramchandran, “Insight into Wavelets from theory to practice” 2nd Ed, PHI
5. Sid Ahmad, “Image Processing, Theory, Algorithm and architecture”, McGraw Hill
COMMUNICATION SYSTEM DESIGN

Teaching Scheme:
Lectures: 3 hrs / week

Examination Scheme:
Theory Paper: 100 Marks (3 Hours)

Designer’s perspective of communication system: Wireless channel description, path loss, multi path fading.


Derivation of NF, IIP3 of Receiver Front End, Partitioning.

Low Noise Amplifier: Introduction, Wideband LNA, Design, Narrow band LNA: Impedance Matching, Narrowband LNA: Core Amplifier,

Active Mixer: Introduction, Balancing, Qualitative Description of The Gilbert Mixer, Conversion Gain, Distortion, And Low-Frequency Case: Analysis of Gilbert Mixer, Distortion, High-Frequency Case, Noise, A Complete Active Mixer, References, Problems. Analog to Digital Converters: Demodulators, A to D Converters used in receivers, Low cost Sigma delta modulators and its implementation.

Design Technology for Wireless Systems: Design entry / simulation, Validation and analysis tools

References:
1. Bosco Leuing, VLSI for Wireless Communication, PE
3. P Gray and R Meyer , Analysis and design of analog integrated circuits, John Wiley & Sons
MOBILE COMMUNICATION

Teaching Scheme:  
Lectures: 3 hrs / week  

Examination Scheme:  
Theory Paper: 100 Marks (3 Hours)

Introduction to Wireless Mobile Communications:
History and evolution of mobile radio systems. Types of mobile wireless services / systems-Cellular, WLL, Paging, Satellite systems, Standards, Future trends in personal wireless systems

Cellular Concept and System Design Fundamentals:
Cellular concept and frequency reuse, Multiple Access Schemes, channel assignment and handoff, Interference and system capacity, trucking and Erlang capacity calculations

Global System for Mobile (GSM):
GSM Architecture and Interfaces; Radio Link features in GSM Systems; GSM Logical Channels and Frame Structure; Messages, Services and Call Flows in GSM; Data Services in GSM: SMS, GSM GPRS; Privacy and Security in GSM.

Mobile Radio Propagation:
Radio wave propagation issues in personal wireless systems, Propagation models, Multipath fading and Base band impulse respond models, parameters of mobile multipath channels, Antenna systems in mobile radio

Modulation and Signal Processing:
Analog and digital modulation techniques, Performance of various modulation techniques-Spectral efficiency, Error-rate, Power Amplification, Equalizing Rake receiver concepts, Diversity and space-time processing, Speech coding and channel coding

System Examples and Design Issues:
Multiple Access Techniques-FDMA, TDMA and CDMA systems, operational systems, Wireless networking, design issues in personal wireless systems

References:
4. Schiller, “Mobile Communications”, Pearson Education Asia Ltd.
North Maharashtra University Jalgaon
M.E. Electronics and Telecommunication / Electronics & Communication (Communication Engg)
W.E.F: 2010-11
First Year Term – II

ANTENNA THEORY & DESIGN

Teaching Scheme: Lectures: 3 Hrs / week
Examination Scheme: Theory Paper: 100 Marks (3 Hrs)


Arrays: Array factor for linear arrays, uniformly excited, equally spaced Linear arrays, pattern multiplication, directivity of linear arrays, non-uniformly excited - equally spaced linear arrays, Mutual coupling, multidimensional arrays, phased arrays, feeding techniques, perspective on arrays.


Aperture Antennas: Techniques for evaluating Gain, reflector antennas - Parabolic reflector antenna principles, Axi - symmetrical parabolic reflector antenna, offset parabolic reflectors, dual reflector antennas, Gain calculations for reflector antennas, feed antennas for reflectors, field representations, matching the feed to the reflector, general feed model, feed antennas used in practice.


References:
Teaching Scheme: Lectures: 3 hrs / week

Examination Scheme: Theory Paper: 100 Marks (3 Hrs)

**Multimedia Communications:** multimedia information representation, multimedia networks, multimedia applications, network QoS and application QoS.

**Information Representation:** text, images, audio and video. Text and image compression, compression principles, text compression, image compression. Audio and video compression, audio compression, video compression, video compression principles, video compression standards: H.261, H.263, P1.323, MPEG 1, MPEG 2. Other coding formats for text, speech, image and video.

**Detailed study of MPEG 4:** coding of audiovisual objects, MPEG 4 systems, MPEG 4 audio and video, profiles and levels. MPEG 7 standardization process of multimedia content description, MPEG 21 multimedia framework, Significant features of JPEG 2000, MPEG 4 transport across the Internet.

**Synchronization:** notion of synchronization, presentation requirements, References model for synchronization, Introduction to SMIL, Multimedia operating systems, Resource management, and process management techniques.

**Multimedia Communication Across Networks:** Layered video coding, error resilient video coding techniques, multimedia transport across IP networks and relevant protocols such as RSVP, RTP, RTCP, DVMRP, multimedia in mobile networks, multimedia in broadcast networks.

**References:**

1. Fred Halsall, “Multimedia Communications”, Pearson education
ERROR CONTROL CODES

Teaching Scheme:
Lectures: 3 Hrs / week

Examination Scheme:
Theory Paper: 100 Marks (3 Hrs)

Introduction to Algebra: Groups, Fields, Binary Field Arithmetic, Construction of Galois Field GF (2m) and its basic properties, Computation using Galois Field GF (2m) Arithmetic, Vector spaces and Matrices.

Linear Block Codes: Generator and Parity check Matrices, Encoding circuits, Syndrome and Error Detection, Minimum Distance Considerations, Error detecting and Error correcting capabilities, Standard array and Syndrome decoding, Decoding circuits, Hamming Codes, Reed – Muller codes.

Cyclic Codes: Introduction, Generator and Parity check Polynomials, Encoding using Multiplication circuits, Systematic Cyclic codes – Encoding using Feedback shift register circuits, Generator matrix for Cyclic codes, Syndrome computation and Error detection, Meggitt decoder, Error trapping decoding, Cyclic Hamming codes.

BCH Codes: Binary primitive BCH codes, Decoding procedures, Implementation of Galois field Arithmetic, Implementation of Error correction. Non – binary BCH codes: q – ary Linear Block Codes, Primitive BCH codes over GF (q), Reed – Solomon Codes, Decoding of Non – Binary BCH and RS codes: The Berlekamp - Massey Algorithm.

Convolutional Codes: Encoding of Convolutional codes, Structural properties, Distance properties, Viterbi Decoding Algorithm for decoding, Soft – output Viterbi Algorithm, Stack and Fano sequential decoding Algorithms, Majority logic decoding.

Majority Logic Decodable Codes: One – Step Majority logic decoding, one – step Majority logic decodable Codes, Two – step Majority logic decoding, Multiple – step Majority logic decoding.

Cryptography: Principles The Data Encryption Standard and Public Key cryptosystems, Asymmetric Encryption Systems, Digital Signature Systems, Basic of digital watermarking

References:
2. Blahut, R.E. “Theory and Practice of Error Control Codes” Addison Wesley
Teaching Scheme: Lectures: 3 Hrs / week

Examination Scheme: Theory Paper: 100 Marks (3 Hrs)

**Embedded System Introduction:** Introduction to Embedded System, History, Design challenges, optimizing design metrics, time to market, applications of embedded systems and recent trends in embedded systems, embedded design concepts and definitions, Custom Single purpose processors, RT level Custom Single purpose processor design, Optimization, General Purpose processors: pipelining, superscalar and VLIW architectures,

**Programmers View:** Instruction set, program and data memory space, I/O, interrupts, operating system, Development environment: design flow and tools, testing and debugging, Application specific instruction set processors (ASIPs), microcontrollers, digital signal processors, less-general AIP environments, selecting microprocessors, and general purpose processor design

**Standard Single Purpose Processors:** peripherals: Introduction, timers, counters and watchdog timers, UART, Pulse width modulators, controlling a DC motor using PWM, LCD controllers, Keypad controllers, stepper motor controllers, ADCs, Real time clocks. Memory: memory write ability and storage permanence, common memory types, composing memory, memory hierarchy and cache, advanced RAM. Memory management, hardware and software design and testing, communication protocols like SPI, SCI, I2C, CAN etc

**Interfacing:** Introduction, Communication basics, Basic protocol concepts, ISA bus protocol: memory access, Arbitration, Priority arbiter, Daisy chain Arbitration, Network oriented Arbitration methods, multilevel bus architectures, Advanced communication principles, Parallel and serial communication, wireless communication, Layering, error detection and correction, serial protocols, parallel protocols, wireless protocols: IrDA, Bluetooth, IEEE802.11

**System Architecture:** ARM7/ARM9 architecture, instruction set, thumb Instruction set, Pipeline, memory management, Bus architecture, Programming concepts, Embedded programming in c and C++. Multiprocessors Scheduling: Model of multiprocessor and distributed systems, Multiprocessor priority ceiling protocol, Elements of scheduling algorithms for end-to-end periodic tasks, Schedulability of fixed priority end-to-end periodic tasks, end-to-end tasks in heterogeneous systems.

**Real Time Systems:** Characterizing real time systems and tasks, Performance measures, Estimating program runtimes, Task assignment and scheduling, Real time operating systems (RTOS), Task management, Race condition, Inter-task communication, Implementation aspects and estimation modeling in embedded systems, Validation and debugging of embedded systems, Real time communication, Hardware-software co-design in an embedded system, Applications of Real time systems.

**References:**
4. Atmel / ARM Data books.
7. Jane Liu, “Real Time Systems”, Pearson India
Teaching Scheme:
Lectures: 3 Hrs / week

Examination Scheme:
Theory Paper: 100 Marks (3 Hours)


Time Domain Models for Speech Processing: Time dependent processing of speech, Short time energy and average magnitude, Short time average zero crossing rate, Speech vs silence discrimination using energy and zero crossings, Pitch period estimation, Short time autocorrelation function, Short time average magnitude difference function, Pitch period estimation using autocorrelation function, Median smoothing.


Homomorphic Speech Processing: Homomorphic systems for convolution, Complex cepstrum, Pitch detection, Formant estimation, Homomorphic vocoder.

Linear Predictive Coding of Speech: Basic principles of linear predictive analysis, Solution of LPC equations, Prediction error signal, Frequency domain interpretation, Relation between the various speech parameters, Synthesis of speech from linear predictive parameters, Applications.


Automatic Speech Recognition: Introduction, Speech recognition vs. Speaker recognition, Signal processing and analysis methods, Pattern comparison techniques, Hidden Markov Models, Artificial Neural Networks.

Audio Processing: Auditory perception and psychoacoustics - Masking, frequency and loudness perception, spatial perception, Digital Audio, Audio Coding - High quality, low-bit-rate audio coding standards, MPEG, AC-3, Multichannel audio - Stereo, 3D binaural and Multichannel surround sound.

References:
Teaching Scheme: 
Lectures: 3 Hrs / week 

Examination Scheme: 
Theory Paper: 100 Marks (3 Hours) 


**Fuzzy Sets:** Classical sets, fuzzy sets, Basic fuzzy properties and operations, Fuzzy relations, Fuzzy tolerance and equivalence relations, membership functions, fuzzy arithmetic, numbers and vectors. 

**Fuzzy Rules and Fuzzy Reasoning:** Extension principle, fuzzy If-Then rules, Fuzzy reasoning. 

**Fuzzy Inference and Decision Making:** natural language, linguistic hedges, rule based systems candrical rule forms, decomposition of compound rules, likelihood and truth quantification, aggregation of fuzzy rules, fuzzy synthetic evaluation, pReferences and consequences, multi objective decision making. 

**Fundamentals of Neural Networks:** basic concepts, model of artificial neural network, neural network architectures, Characteristics of neural networks, Learning methods, Taxonomy of Neural Network Architectures, 

**Back Propagation Networks:** Architecture of back propagation network, Back propagation learning, Selection of various parameters in BPN, Variations of Standard Back propagation algorithm, Associative memory. 

**Classifiers:** Classifiers based on Bay’s decision theory, Bayesian classification for normal distribution, Baysian inference, estimation of unknown probability distribution, bay’s error, linear classifiers, linear discriminant functions and decision hyper planes. the perceptron algorithm, support vector machine(SVM),separable and non separable classes, an introduction to non linear classifiers, the XOR problem, the two layer perceptron and radial basis function(RBF) network, context dependent classification. 

**Evolutionary Algorithms:** Genetic algorithm, cycle of genetic algorithm, crossover, mutation, fitness function, schema, fundamental theorem of GA(schema theorem), differential evolution(DE), modified differential evolution(Mo DE), multi objective optimization using evolutionary algorithms, hybridization with clustering, genetic programming. 

**Hybrid Systems and Applications:** Hybrid systems, Neuro-Fuzzy hybrids, Application of neural network and fuzzy logic in image processing and pattern recognition. 

**References:**
1. Timothy J Ross, “Fuzzy logic with engineering applications”, TMH 
4. George J Klir, Bo Yuan, “Fuzzy sets and fuzzy logic”, PHI 
5. Simon Haykin, “Neural Networks”, Pearson Education 
7. Dan W. Patterson, “Introduction to A.I. & Expert Systems”, PHI 
9. Duda and Hart, “Pattern Recognition”, Willy Publication
North Maharashtra University Jalgaon  
M.E. Electronics and Telecommunication / Electronics & Communication (Communication Engg)  
W.E.F: 2010-11  
First Year Term – II  
LABORATORY PRACTICE-II  

Teaching Scheme: 
Practical: 6 Hrs/week  

Experiments / Assignments based on any three subjects out of which one should be Elective from First Year Term – II syllabus. The concerned subject in-charge should frame minimum of six laboratory experiments or assignments, two from each subject.

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SEMINAR-II  

Teaching Scheme: 
Practical: 4Hrs/week  

Seminar on related state of the art topic of student’s own choice approved by the department.

Term Work  
The term-work & presentation of the Seminar-II will be evaluated by departmental committee consisting of guide and two faculty members of the department appointed by Director / Principal of the college as per the recommendation of the Head of the Department.
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SEMINAR-III

Teaching Scheme:  
Practical: 4Hrs/week

Examination Scheme:  
Term Work: 50 Marks
Oral: 50 Marks

Seminar on special topic: The topic should be on any of the area not included in the regular curriculum. The report should include detailed study of specific concept (i.e. analysis, design & implementation). This can be a theoretical study or practical implementation approved by the department/guide.

Term Work
1. Seminar-III should be conducted at the end of Second Year Term I.

2. The term-work of the Seminar-III will be evaluated by departmental committee consisting of guide and two faculty members of the department appointed by Director / Principal of the college as per the recommendation of the Head of the Department.

3. The Seminar-III presentation will be evaluated by examiners appointed by University, one of which should be the guide.

4. Student must submit the Seminar Report in the form of soft bound copy

5. The marks of seminar-III should be submitted at the end of Second Year Term I to the University.

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Second Year Term – I

PROJECT STAGE-I

Teaching Scheme:  
Practical: 18 Hrs / week

Examination Scheme:  
Term Work: 100 Marks

Project will consist of a system Development in Software / Hardware. Project Work should be carried out using Software Engineering principles and practices.

Term Work
The term-work of the Project Stage-I will be evaluated by departmental committee consisting of guide and two faculty members of the department appointed by Director / Principal of the college as per the recommendation of the Head of the Department.
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PROGRESS SEMINAR

1. Progress Seminar should be conducted in the middle of Second Year Term II.
2. The Progress Seminar Term-Work will be evaluated by departmental committee consisting of guide and two faculty members of the department appointed by Director / Principal of the college as per the recommendation of the Head of the Department.
3. Student must submit the progress report in the form of soft bound copy.
4. The marks of progress seminar should be submitted along with the marks of Project Stage II

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PROJECT STAGE-II

Teaching Scheme:
Practical: 18 Hrs / week

Examination Scheme:
Term Work: 150 Marks
Oral: 100 Marks

This is in continuation of Project Stage-I. The complete System Development in software / hardware carried out using Software Engineering principles and practices is expected. It should be a working system either software or hardware or combination of both.

He / she has to present / publish at least one paper in reputed National / International Journal / Conference on his / her Project work before submission of his / her Thesis / Dissertation.

Term Work

1. The Term Work of Project Stage –II will be assessed jointly by the pair of Internal (Guide) and External examiner along with oral examination of the same.